

Notice of References Cited	Application/Control No. 10/044,731	Applicant(s)/Patent Under Reexamination PRAKASH, RAJ	
	Examiner Jason Mitchell	Art Unit 2193	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,189,141	02-2001	Benitez et al.	717/153
	B	US-6,158,049	12-2000	Goodwin et al.	717/158
	C	US-6,631,516	10-2003	Baumgart et al.	717/143
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Schilling, J.L. "Optimizing Away C++ Exception Handling" ACM SIGPLAN Notices V.33(8), pp.40-47, 8/1998.
	V	Kawahito, M et al. "Effective Null Pointer Check Elimination Utilizing Hardware Trap", Proceedings of the ninth international conference on Architectural support for programming languages and operating systems, pp. 139-149, 11/2000
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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